

UNITED STATES PATENT AND TRADEMARK OFFICE
CERTIFICATE OF CORRECTION

PATENT NO. : 6,978,319 B1
DATED : December 20, 2005
INVENTOR(S) : Michael D. Rostoker and Joel Silverman

Page 1 of 1

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

Column 8.

Lines 15-34, should read

-- 1. A method for plug-and-play compatibility between a first apparatus capable of digitally communicating with a first protocol through a first apparatus connector and a second apparatus capable of digitally communicating with a second protocol through a second apparatus connector, said method comprising:

determining a first communication protocol received at a first apparatus connector;

enabling a first protocol driver appropriate to said determined first protocol in a first protocol processor;

translating digital communications provided at said first apparatus connector in said first protocol into digital communications at said second apparatus connector in said second protocol through said first protocol processor and then a second protocol processor; and

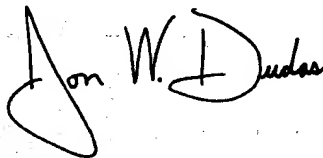
translating digital communications provided at said second apparatus connector in said second protocol into digital communications at said first apparatus connector in said first protocol through said second protocol processor and then said first protocol processor. --.

Lines 62-65, should read

-- 6. A single chip protocol translator as recited in claim 4, wherein at least one of said first driver and said second driver is loaded into said memory from at least one of said first external devices and said second external devices. --.

Signed and Sealed this

Twenty-fifth Day of April, 2006



JON W. DUDAS

Director of the United States Patent and Trademark Office